

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 09/540,729 GURA ET AL.	
		Examiner	Art Unit	Page 1 of 1 Dmitry Levitan 2662

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
B	US-			
C	US-			
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FOREIGN PATENT DOCUMENTS

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N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Cell-level path allocation in a three-stage ATM switch Collier, M.; Curran, T.; Communications, 1994. ICC 94, SUPERCOMM/ICC '94, Conference Record, Serving Humanity Through Communications. IEEE International Conference on , 1-5 May 1994 Pages:1179 - 1183
	V	Path allocation in a three-stage broadband switch with intermediate channel grouping Collier, M.; Curran, T.;INFOCOM '93. Proceedings.Twelfth Annual Joint Conference of the IEEE Computer and Communications Societies. IEEE, 28 March- April 1993, p. 927-934
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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